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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :  
Akira KABESHITA et al. : **Confirmation No. 9982**  
Serial No. 10/568,501 : **Mail Stop: PCT**  
Filed February 16, 2006 : **Attorney Docket No. 2006\_0163A**

APPARATUS AND METHOD FOR REMOVING  
SEMICONDUCTOR CHIP

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

A corrected filing receipt is requested in the above-referenced case. A copy of the original filing receipt is enclosed, with the changes noted thereon.

Expedited issuance of the corrected filing receipt is respectfully requested.

Respectfully submitted,

Akira KABESHITA et al.

By Michael S. Huppert

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December 4, 2006

CORRECTED COPY



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
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2006-0163A

CONFIRMATION NO. 9982

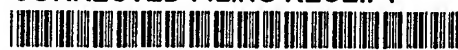
52349

WENDEROTH, LIND & PONACK L.L.P.  
2033 K. STREET, NW  
SUITE 800  
WASHINGTON, DC 20006

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CORRECTED FILING RECEIPT



\*OC000000020208628\*

WENDEROTH, LIND & PONACK

Date Mailed: 08/28/2006

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**Applicant(s)**

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**Power of Attorney:** The patent practitioners associated with Customer Number 52349.

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/JP04/13281 09/07/2004

**Foreign Applications**

JAPAN 2003-315269 09/08/2003

If Required, Foreign Filing License Granted: 07/21/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/568,501**

**Projected Publication Date:** 12/07/2006

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Apparatus and method for removing semiconductor chip

**Preliminary Class**

414

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